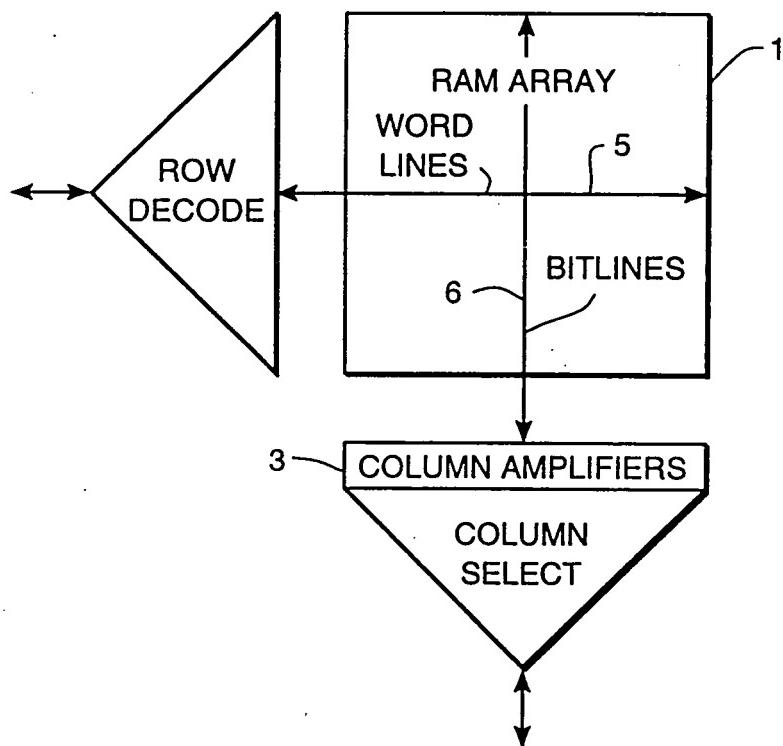


FIG 1



11

— FIG — 2

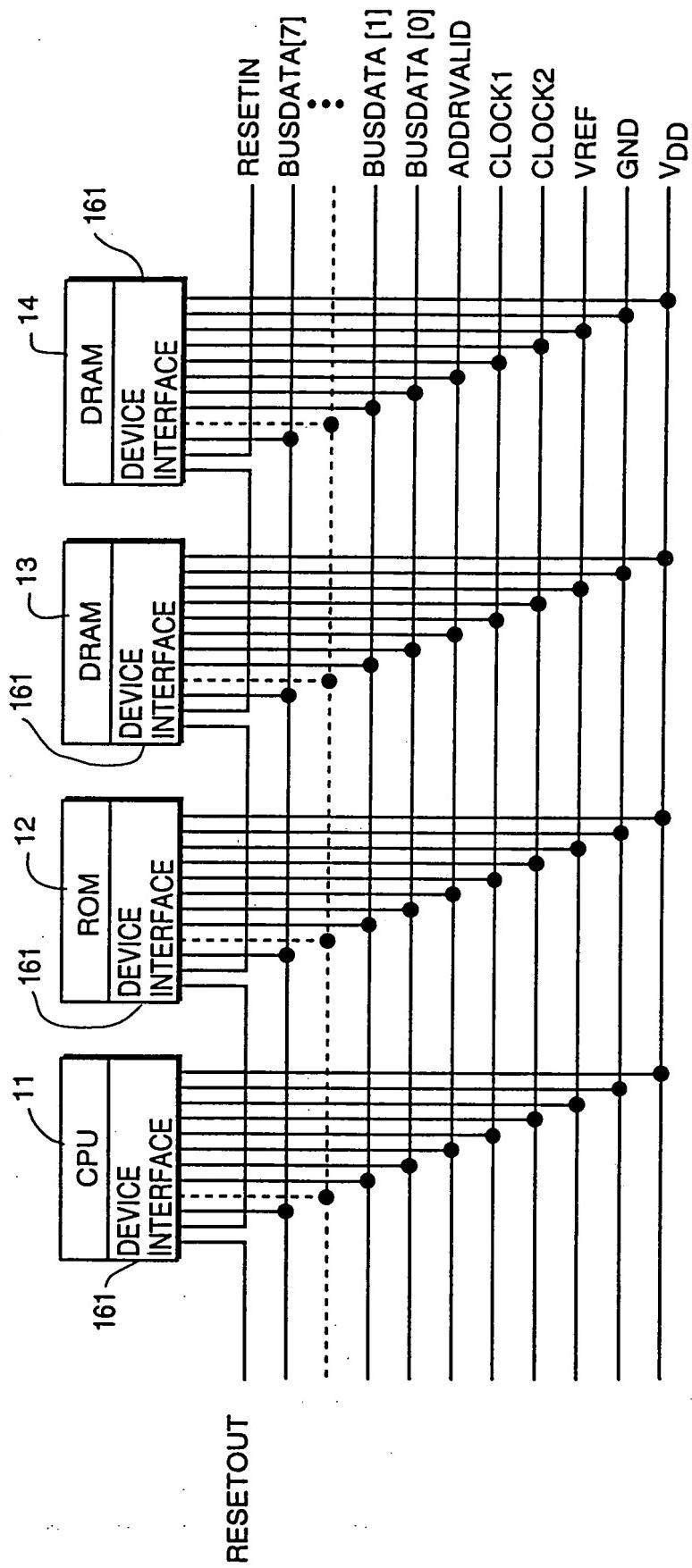
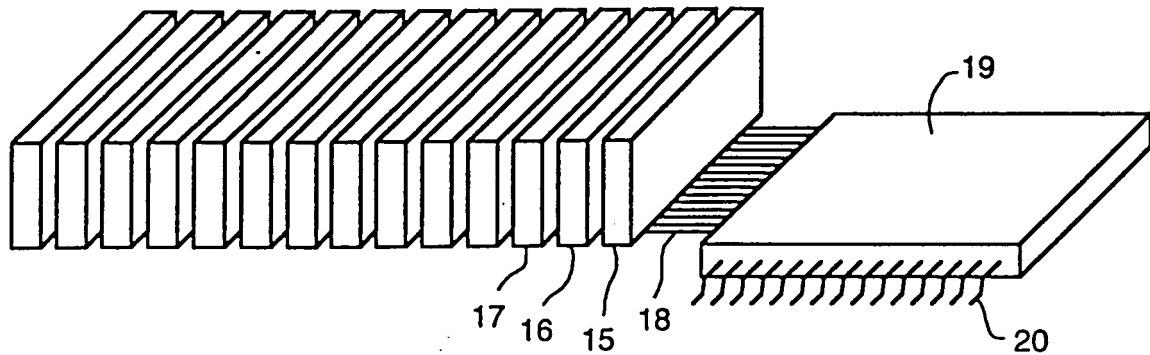


FIG 3



REGULAR ACCESS

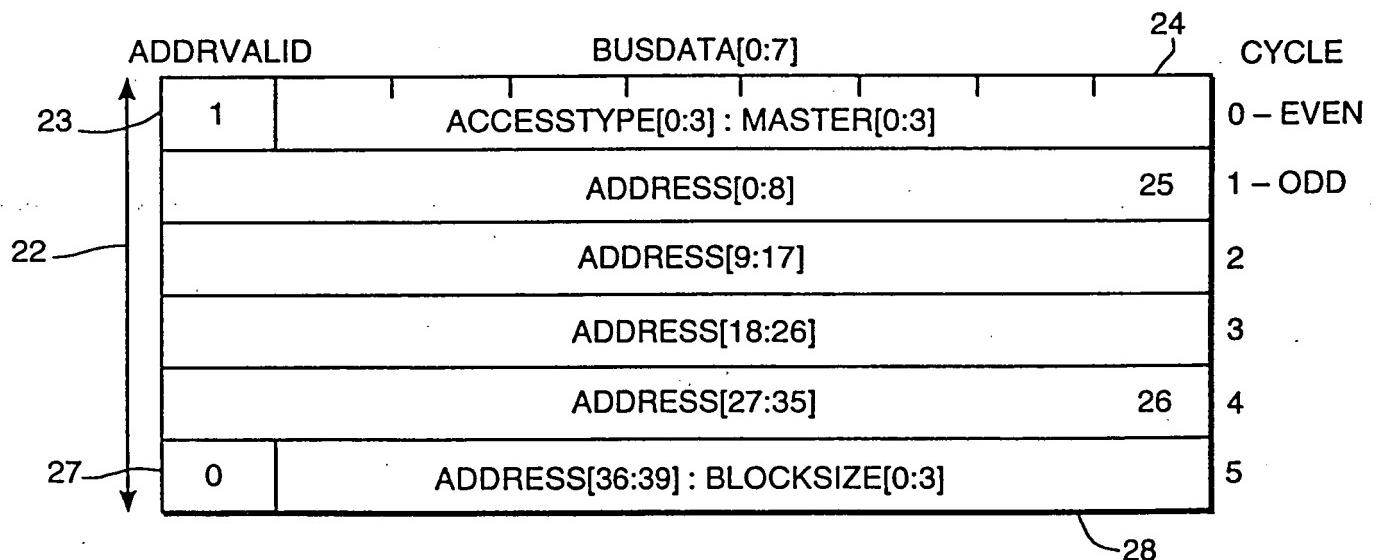
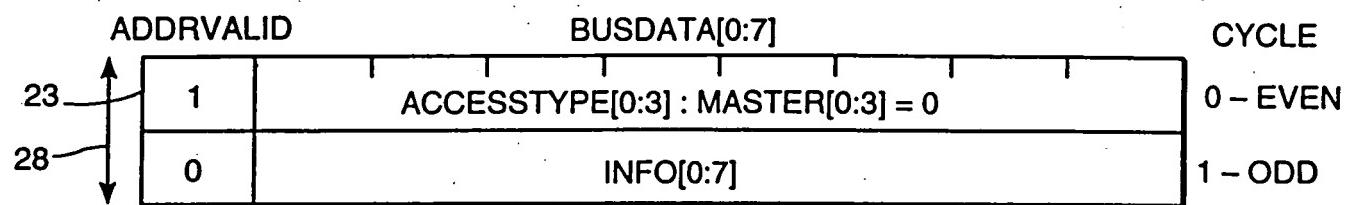


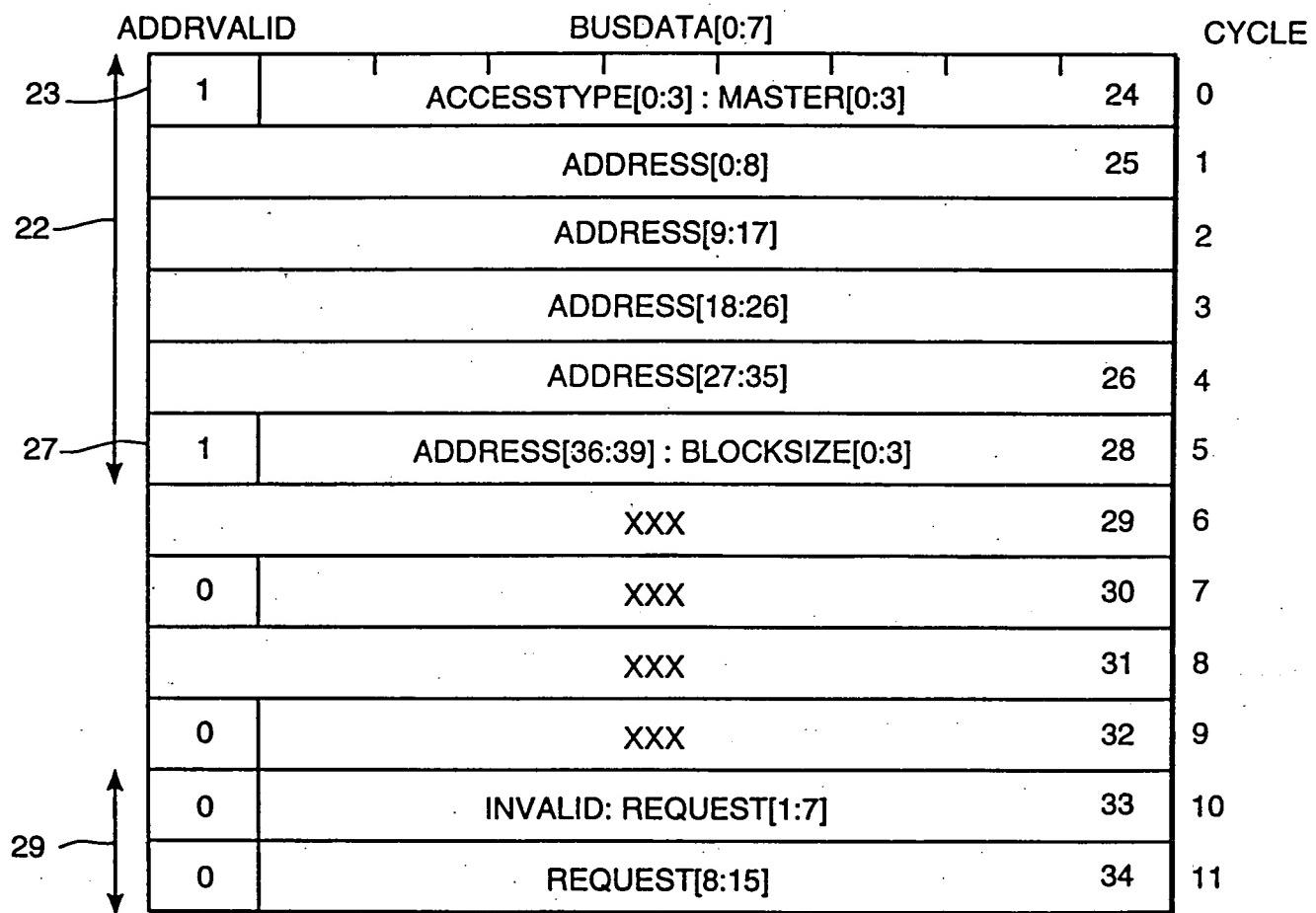
FIG 4

0 9 5 4 6 5 4 8 = 0 4 4 0 0 0

REJECT (NACK) CONTROL PACKET



FIG_5



FIG_6

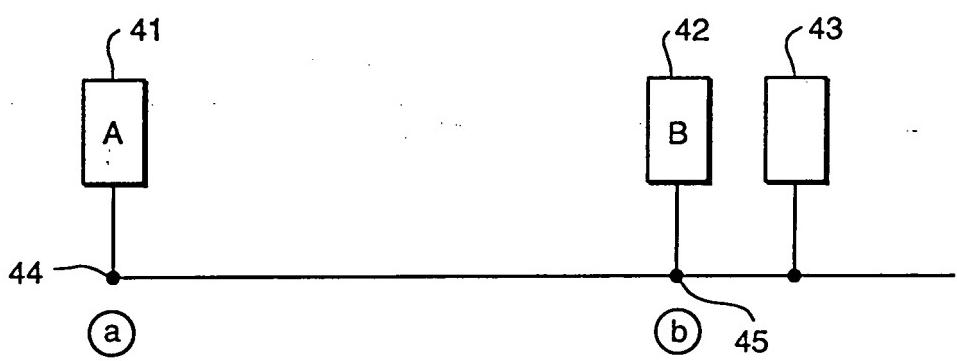
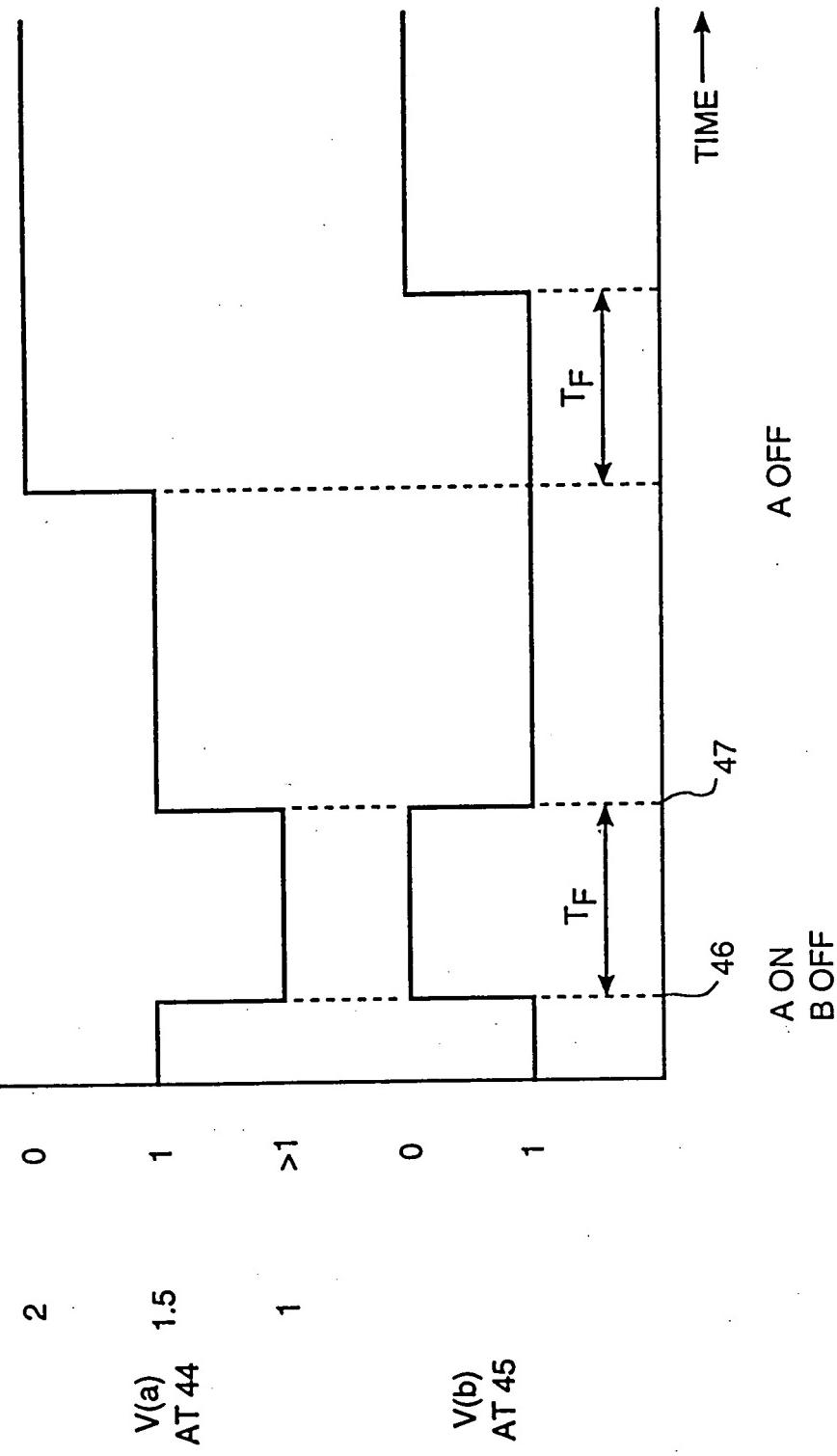


FIG. 7A

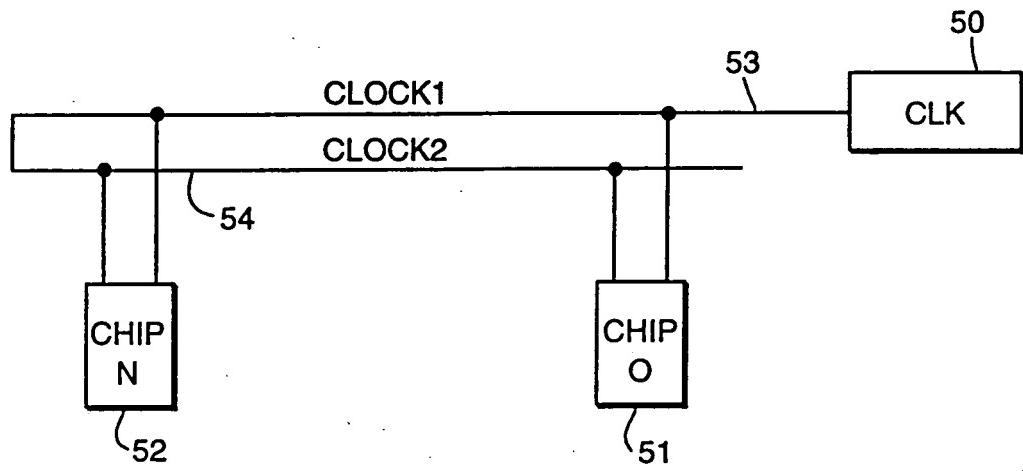
0 0 0 0 0 0 0 0 0 0 0 0

LOGICAL
VOLTAGE VALUE

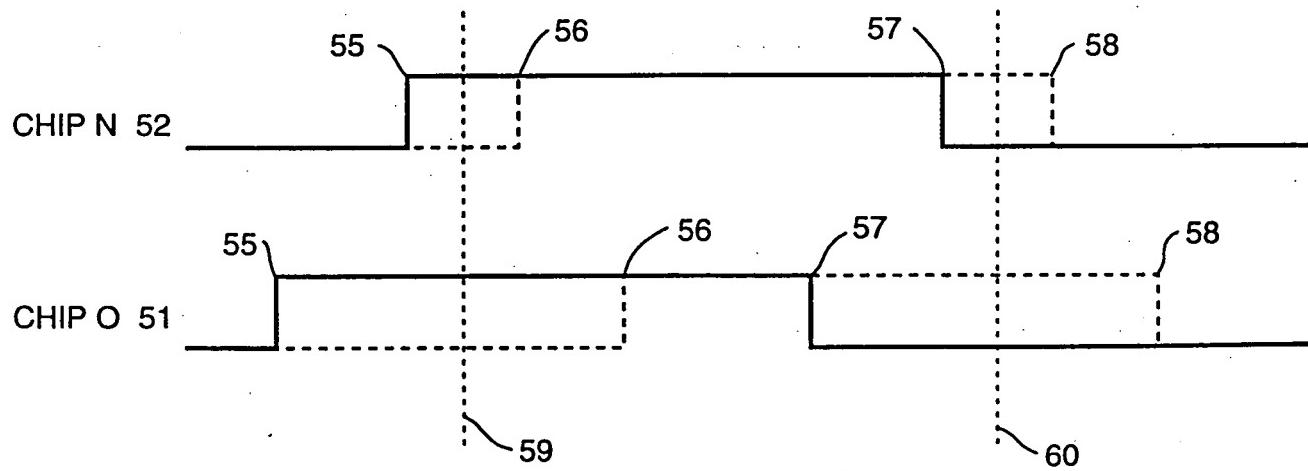


TIME →

TE-TIE-ZH



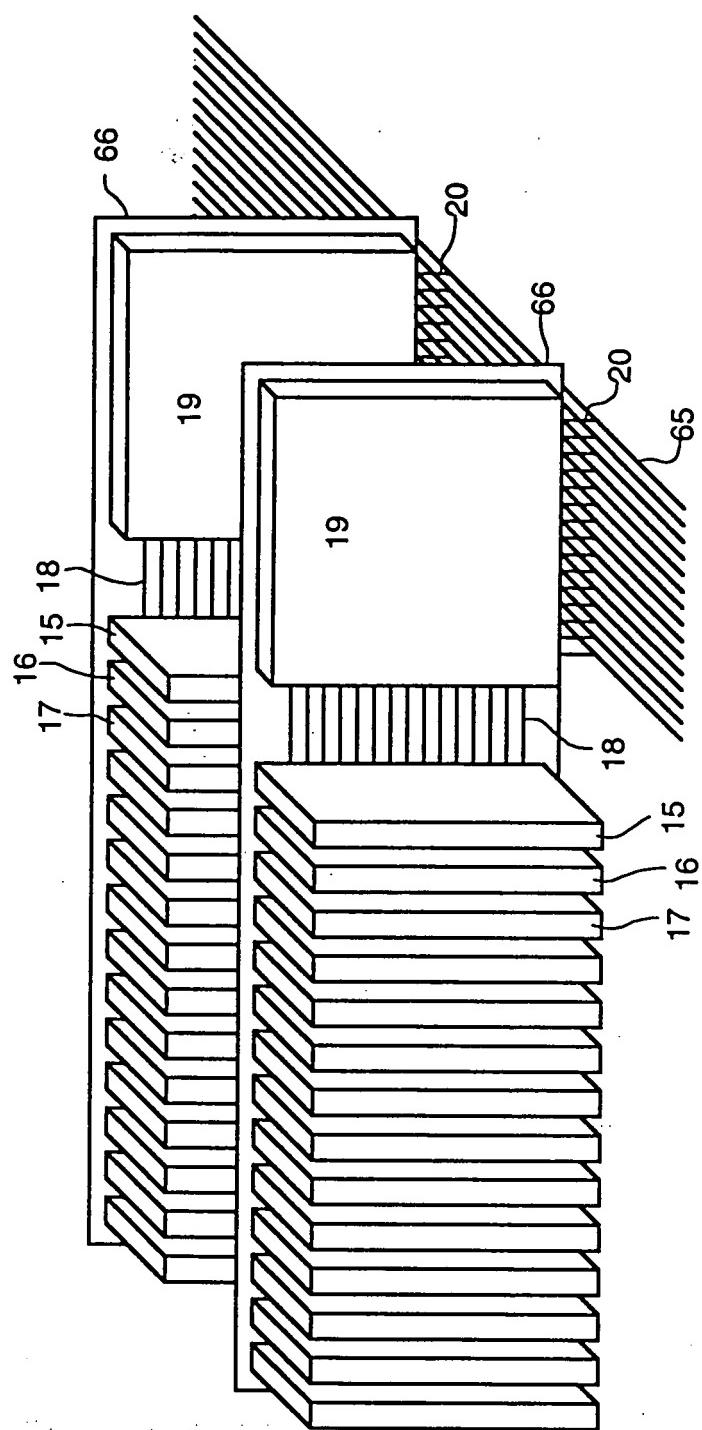
FIG_XA



FIG_XB

0954534-13 - 0441000

FIG. 6



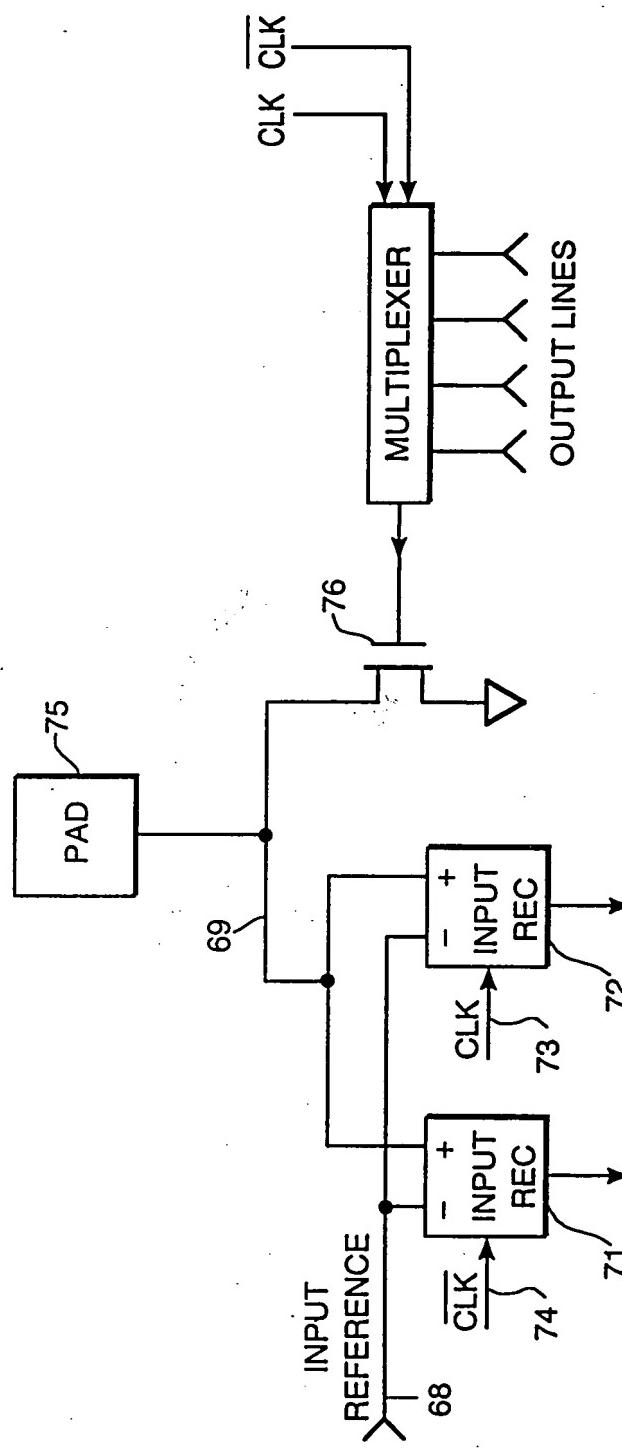
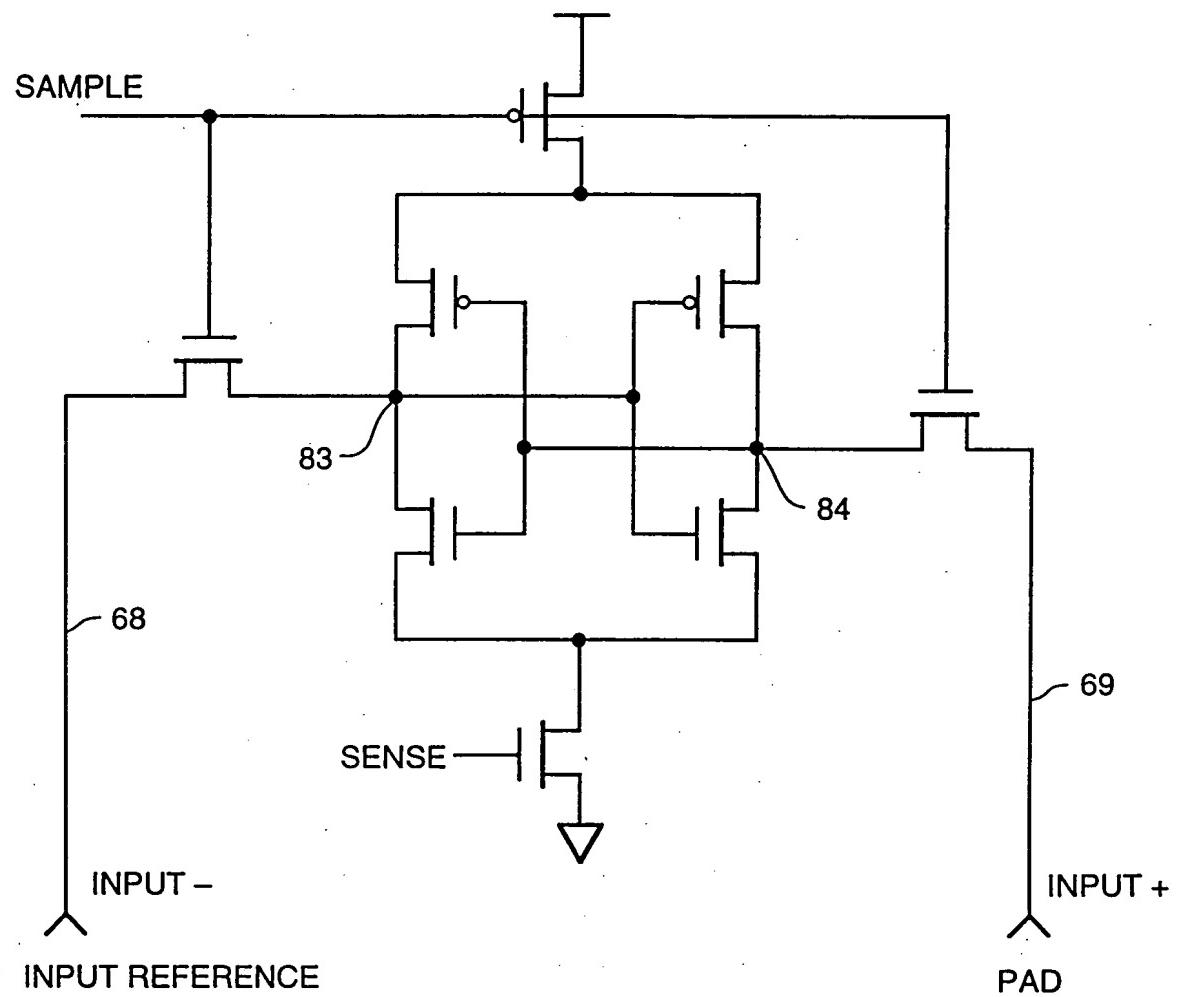


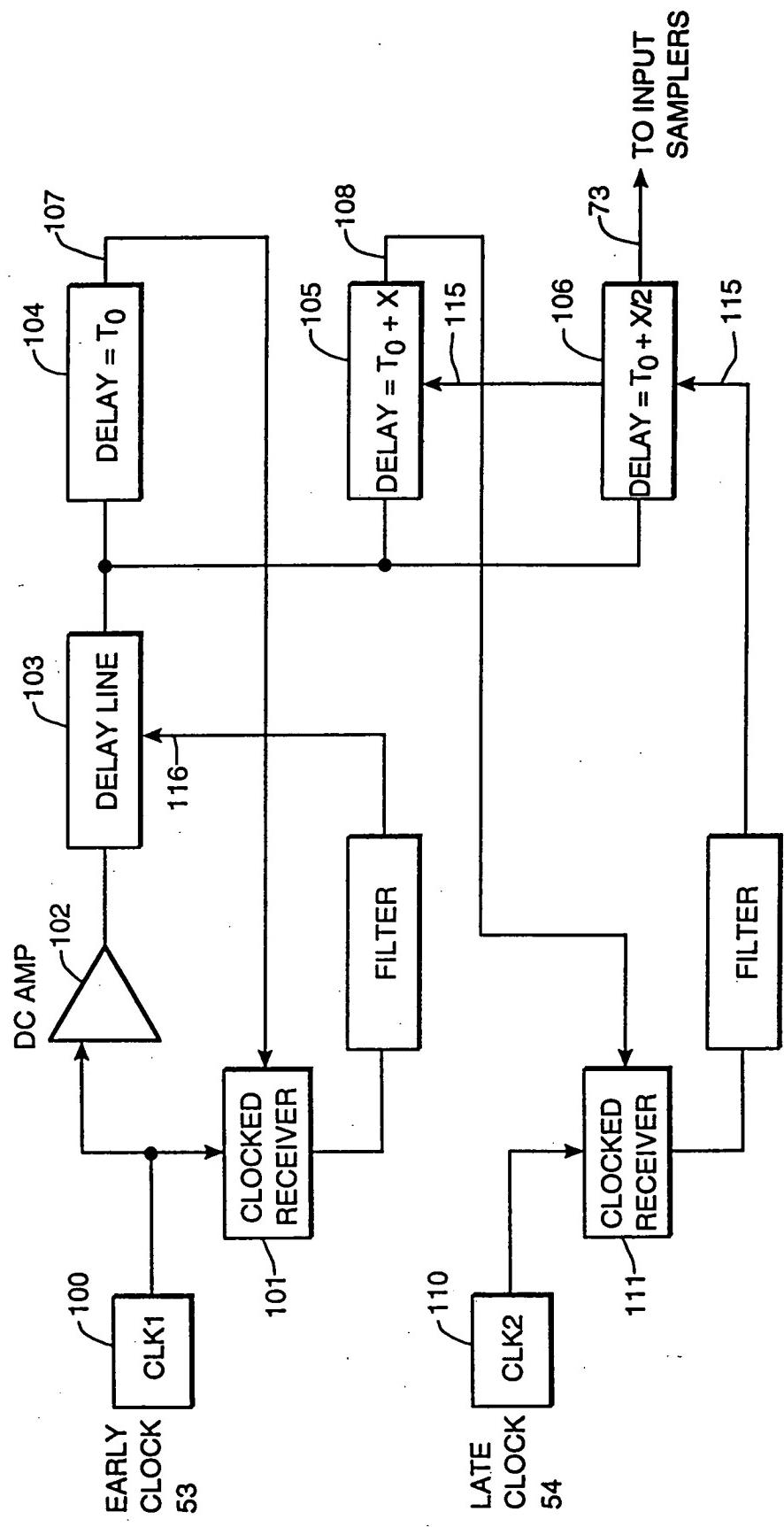
FIGURE 1

FIG. 11

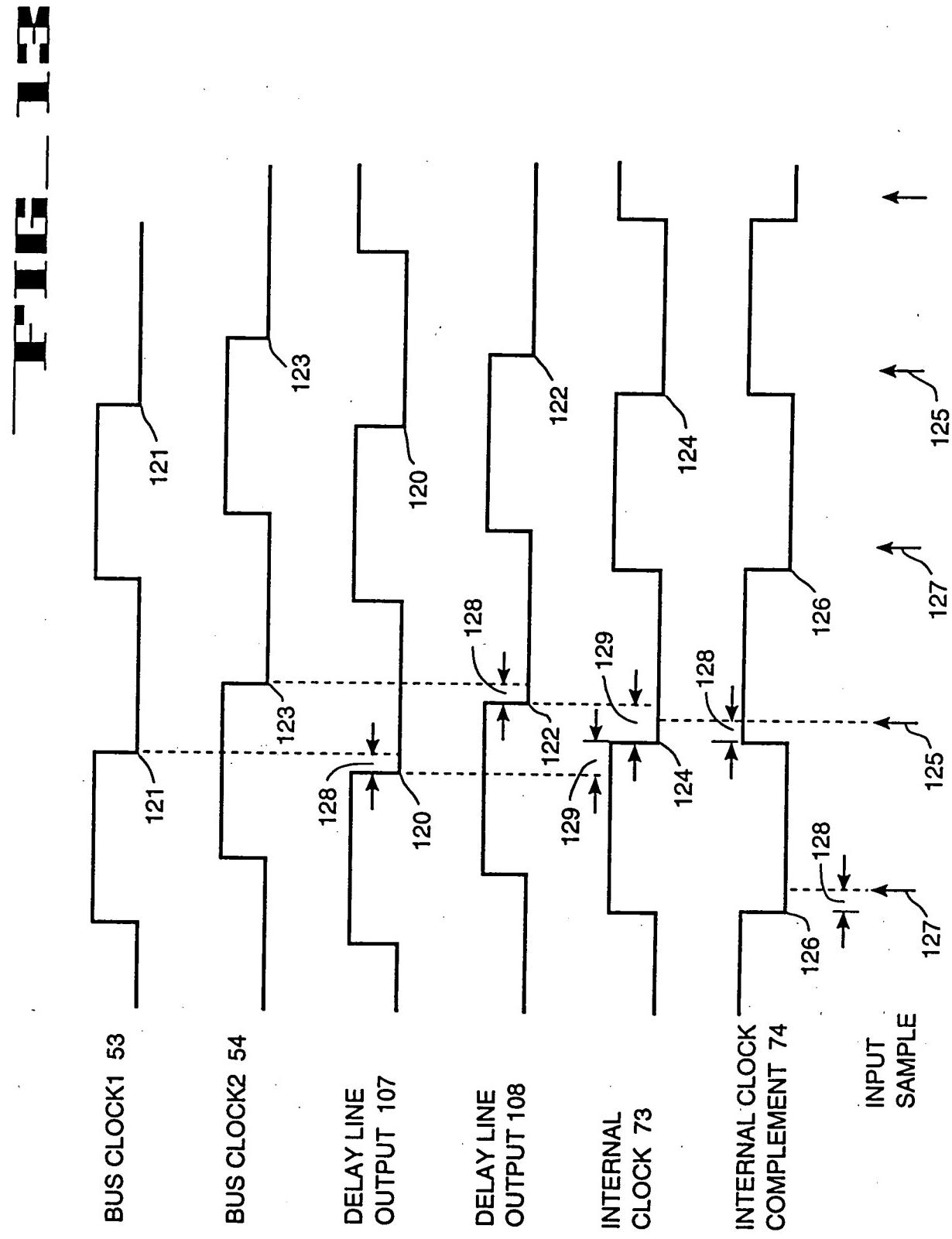


00545648 0041000

FIG_12



© C O T F T U G " S H S S A H S G C



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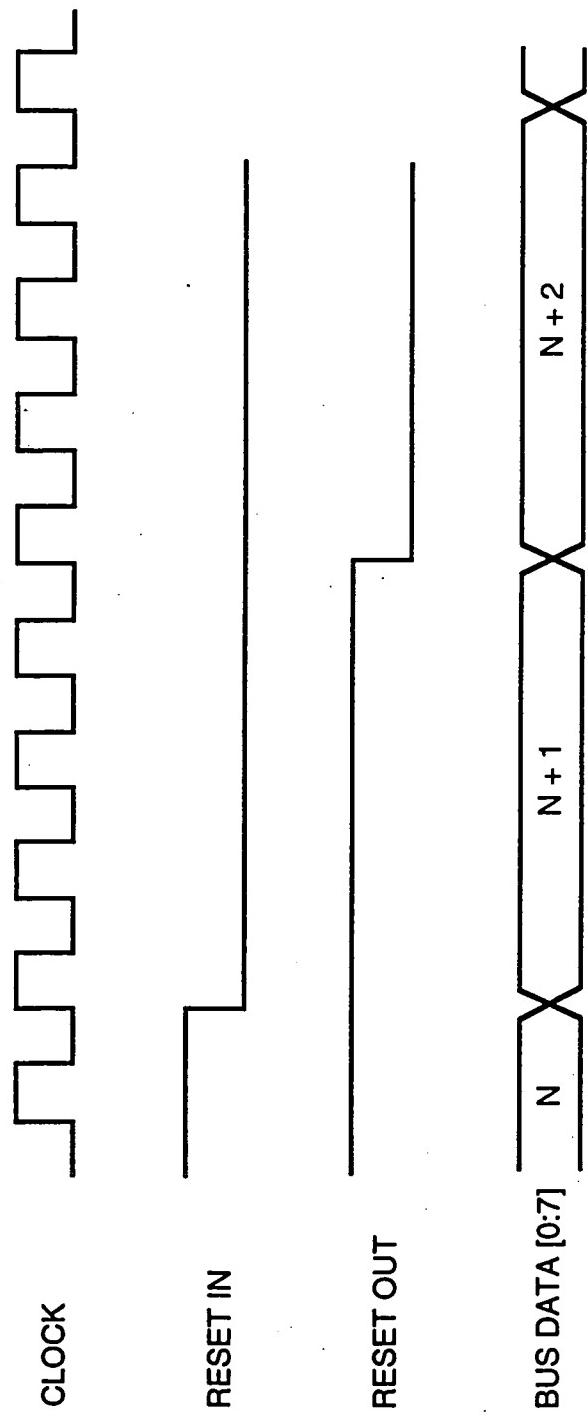
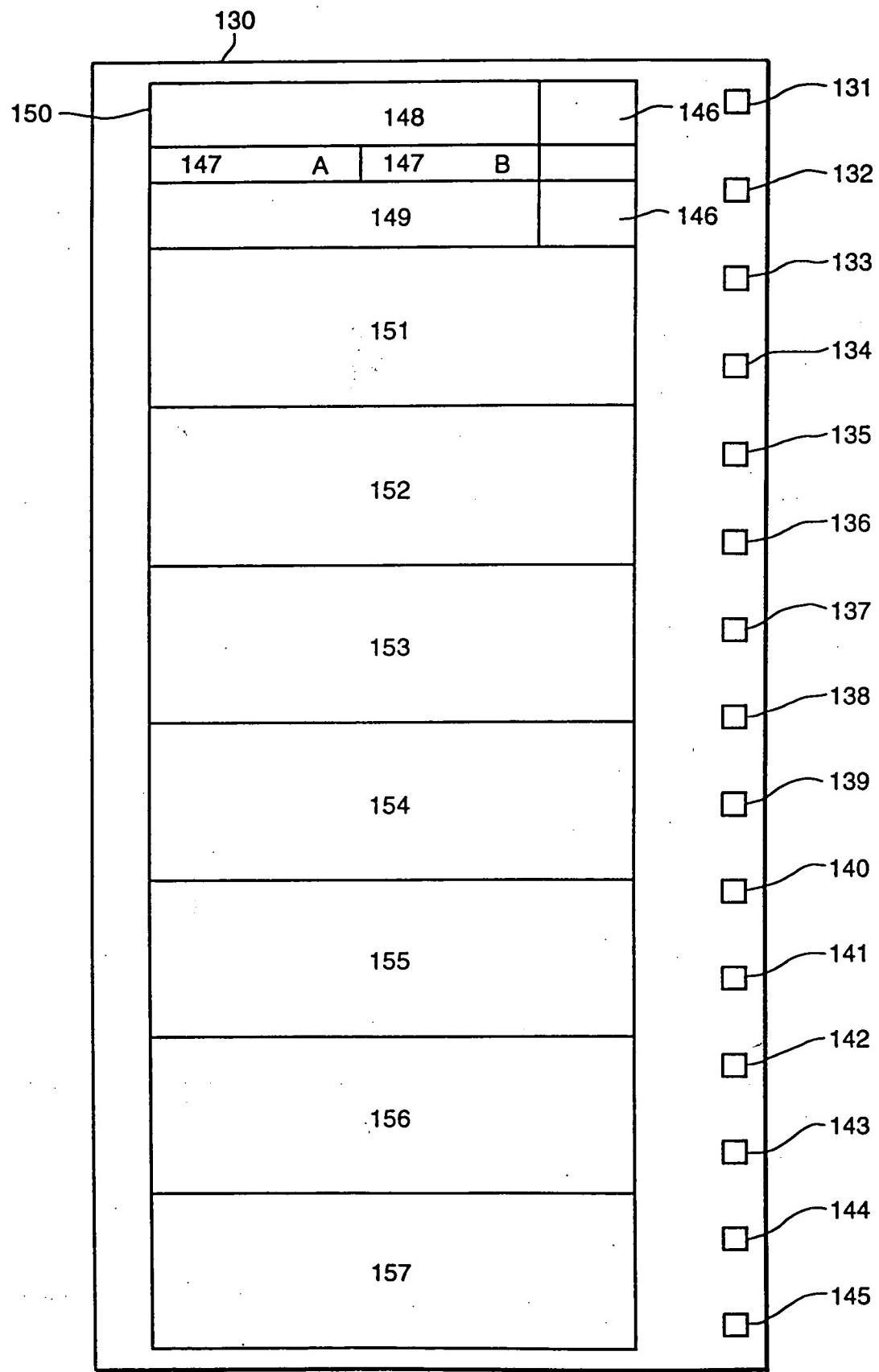


FIGURE 14-

FIG_15



000545648 - 004 00000